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09/849047  
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	Subclass	ISSUE CLASSIFICATION	
	Class		

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED NS (3) Q.A. CM	

CLASS	CROSS-REF	ART UNIT	EXAMINER
APPLICATION NO. 09/849047	CONT/PRIOR	CLASS 257	SUBCLASS
			ART UNIT 2811
EXAMINER Lewis			
APPLICANTS Gabriel Li Kemelm Murray Jose Arreola Shahin Sharifzadeh			
TITLE Buried layer substrate isolation in integrated circuits			

PTO-2040  
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date). has been disclaimed.	(Assistant Examiner) _____ (Date) _____			<b>NOTICE OF ALLOWANCE MAILED</b> _____ _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Primary Examiner) _____ (Date) _____			<b>ISSUE FEE</b> _____ _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instrument) (Inventor) (Date)			<b>ISSUE BATCH NUMBER</b> _____	
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